

FIG. 1

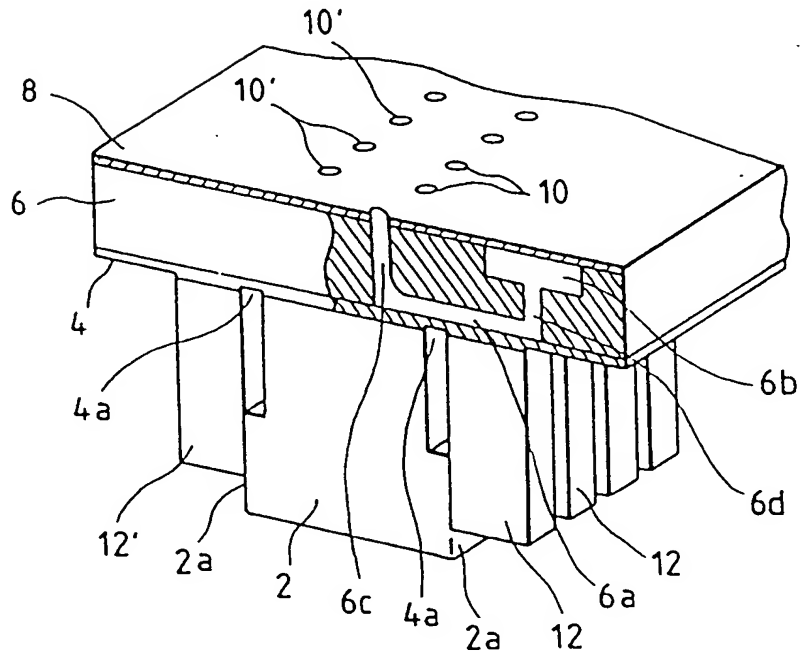


FIG. 2

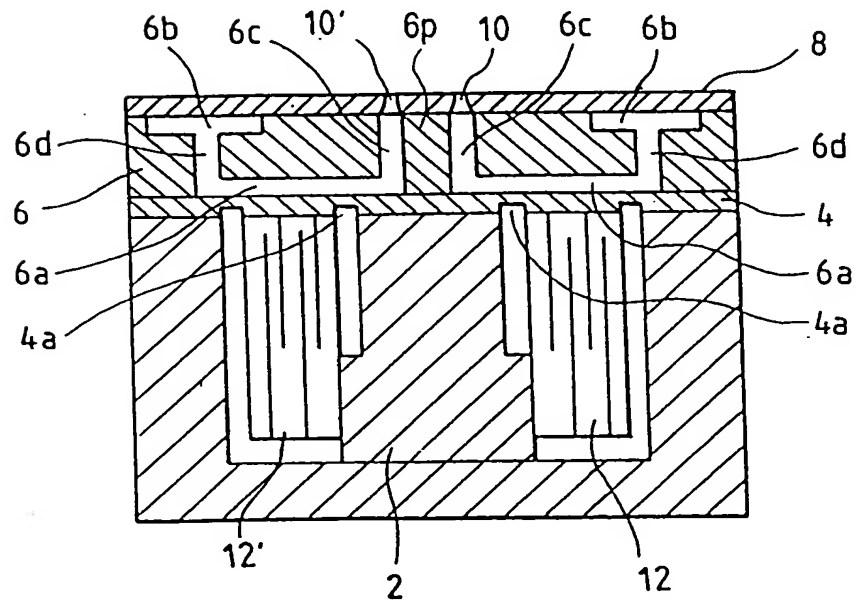


FIG. 3a

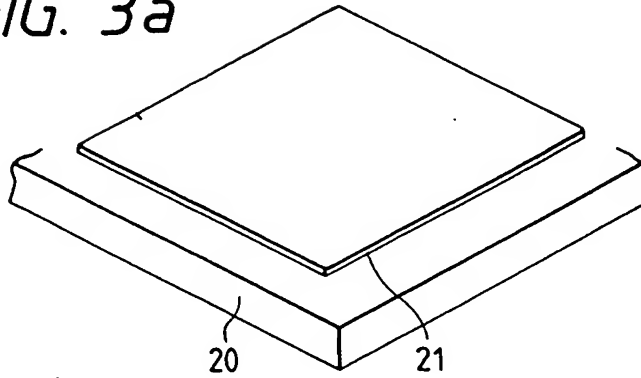


FIG. 3b

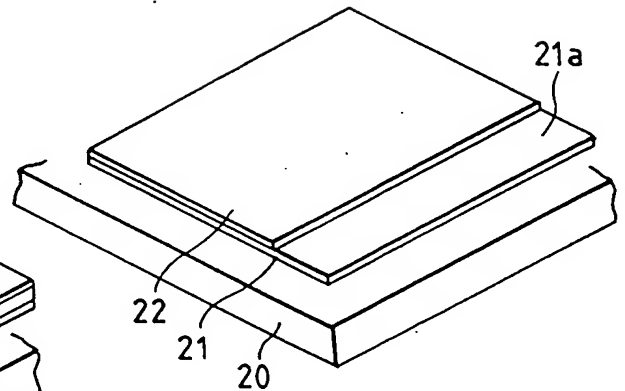


FIG. 3c

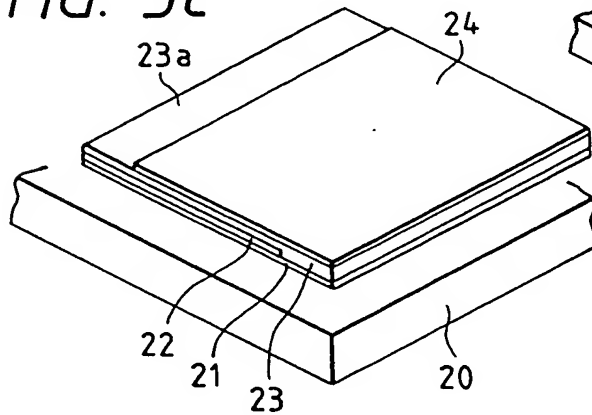


FIG. 3d

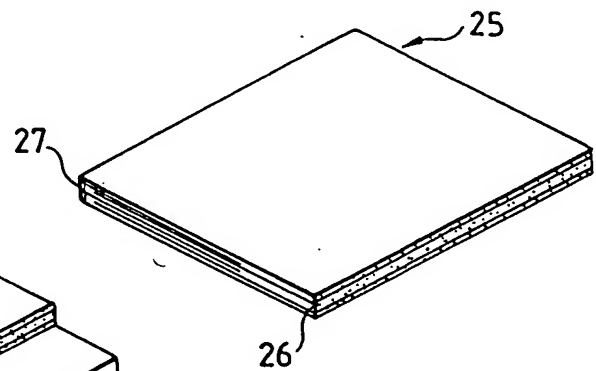


FIG. 3e

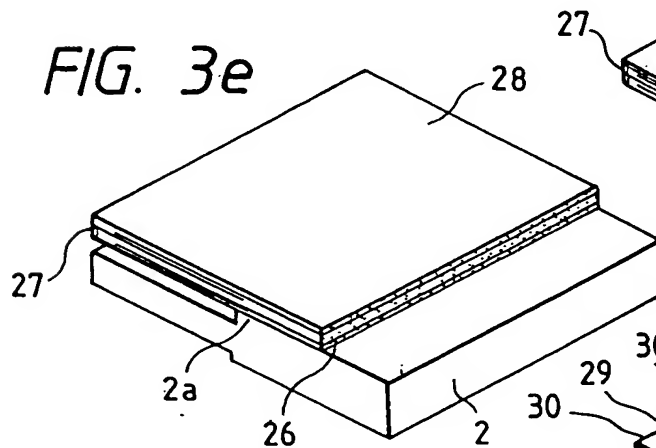


FIG. 3f

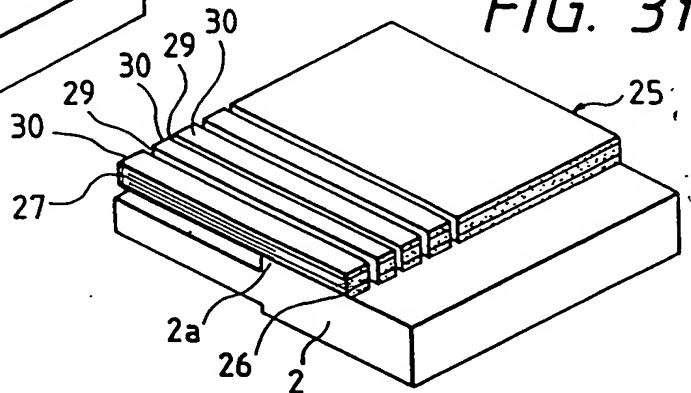


FIG. 4

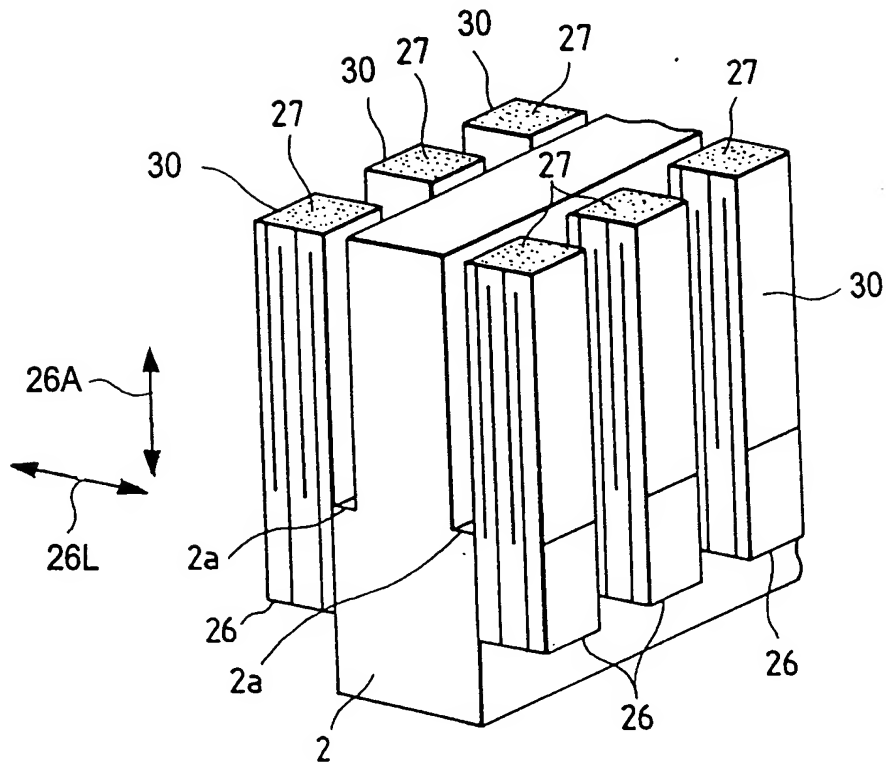


FIG. 5

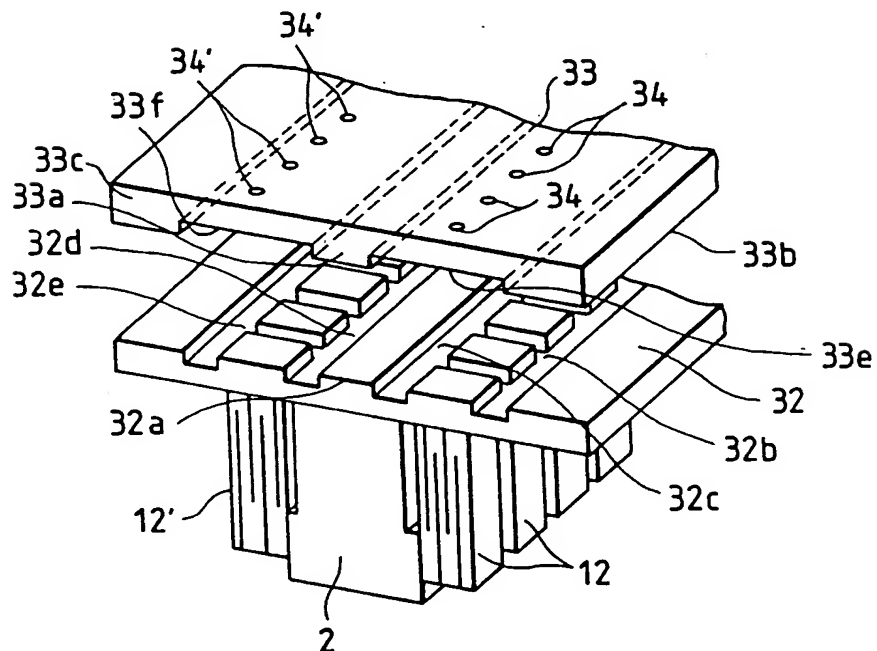


FIG. 6a

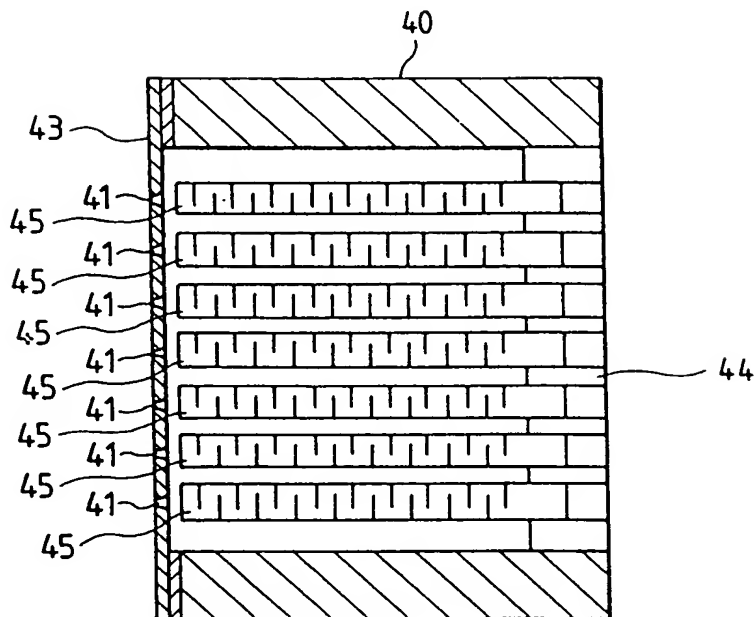


FIG. 6b

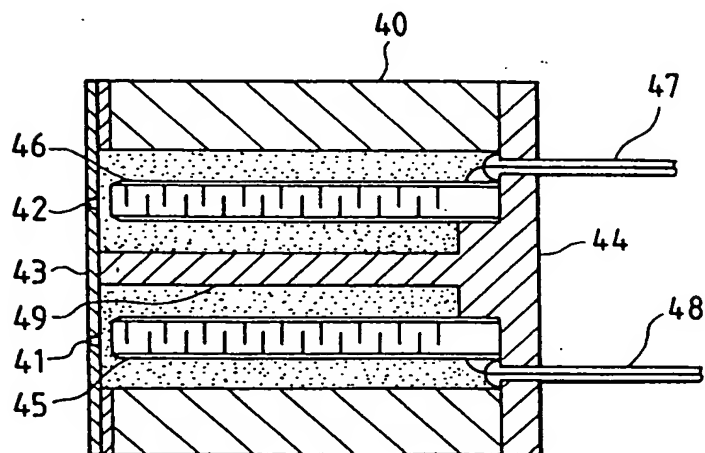


FIG. 7a

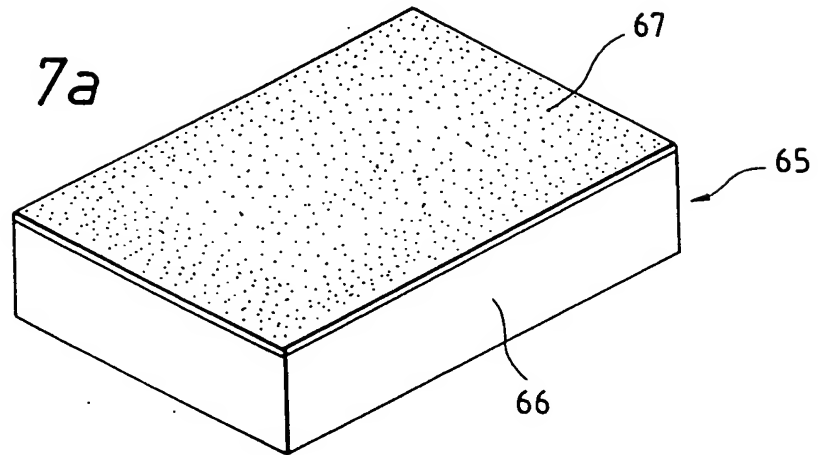


FIG. 7b

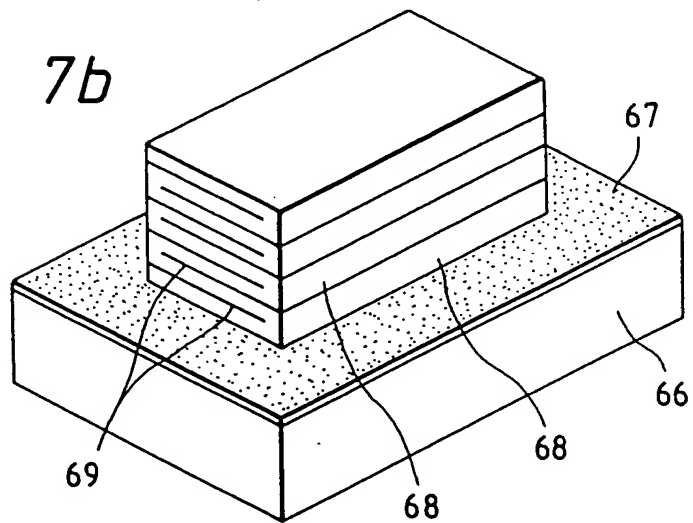


FIG. 8

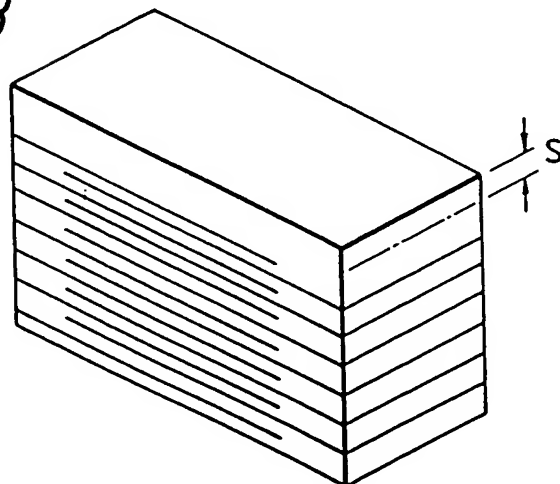


FIG. 9

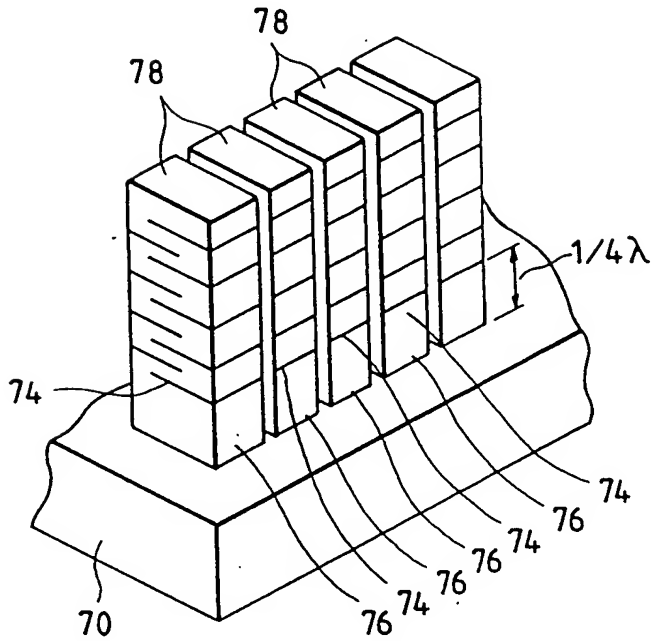


FIG. 10

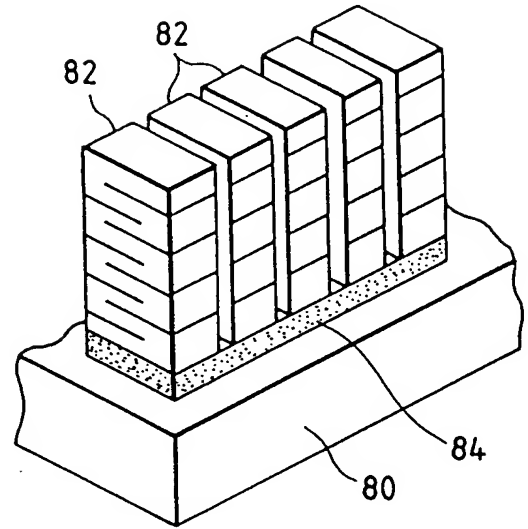


FIG. 11

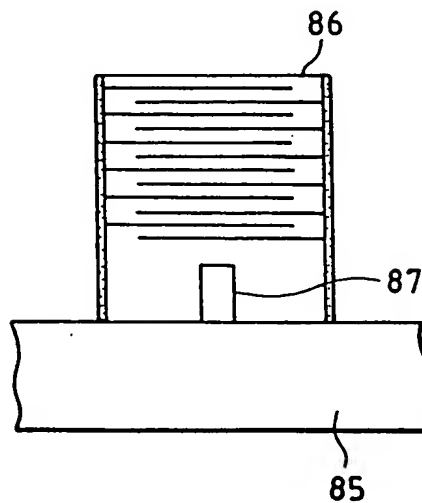


FIG. 12

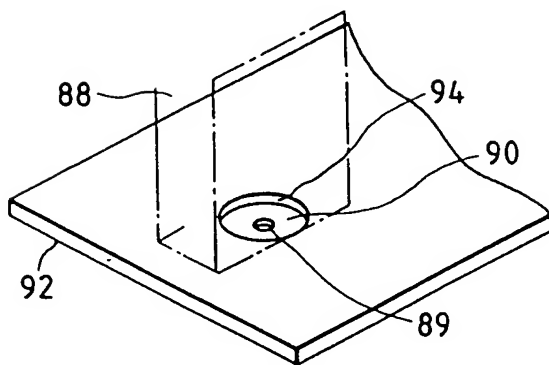


FIG. 15

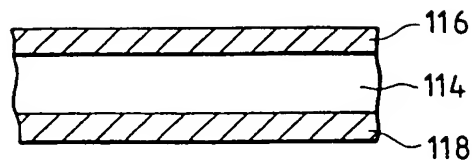


FIG. 16

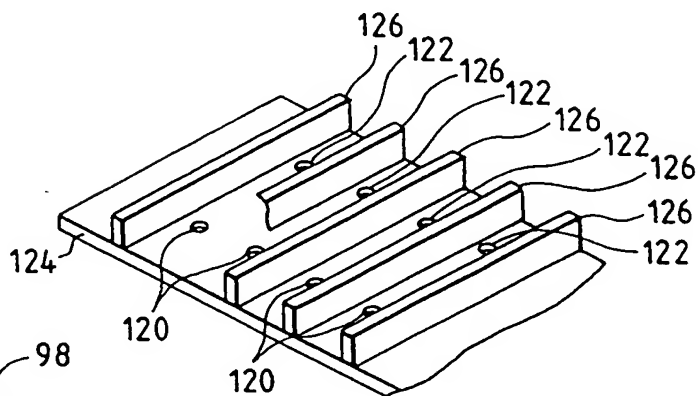


FIG. 13

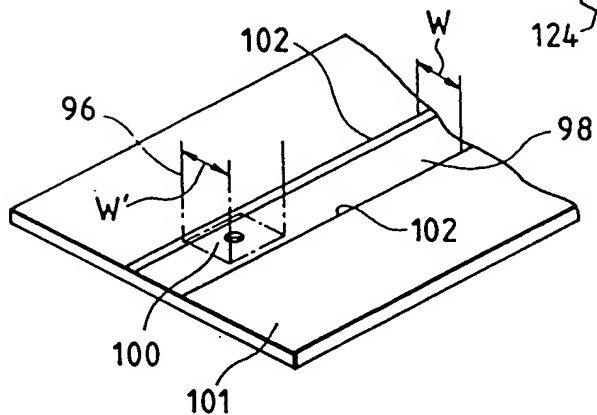


FIG. 17

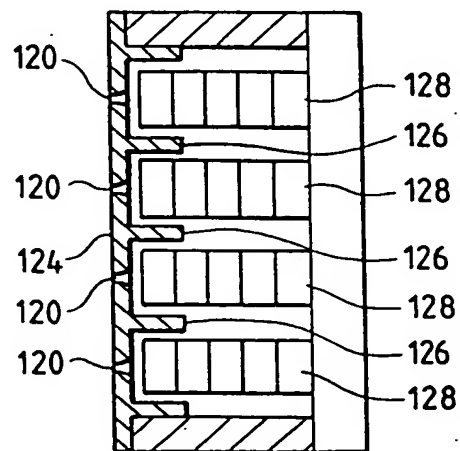


FIG. 14

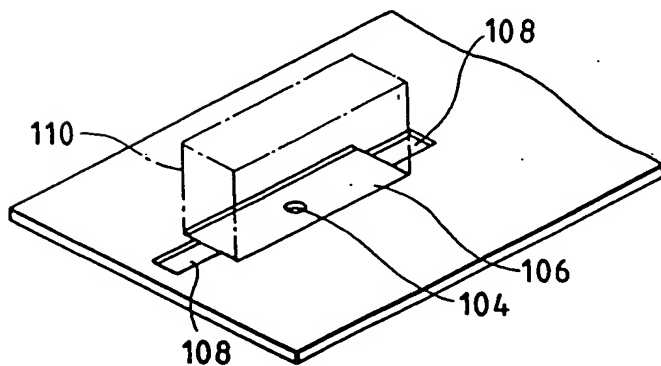


FIG. 18

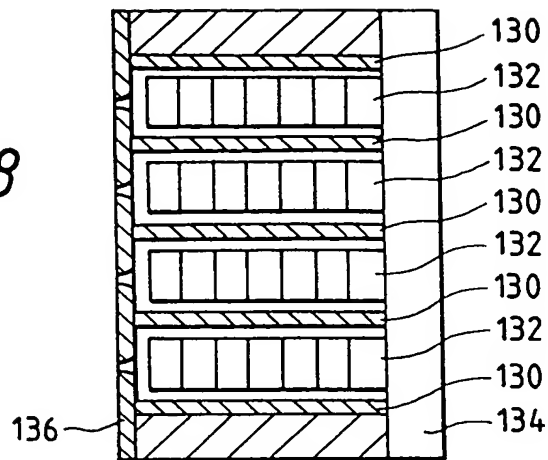


FIG. 19

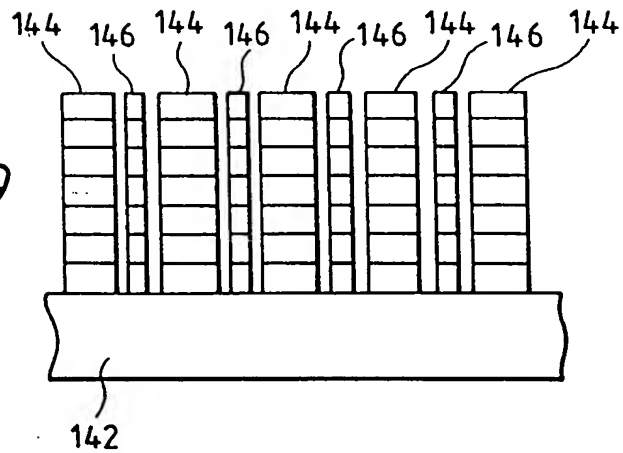


FIG. 20

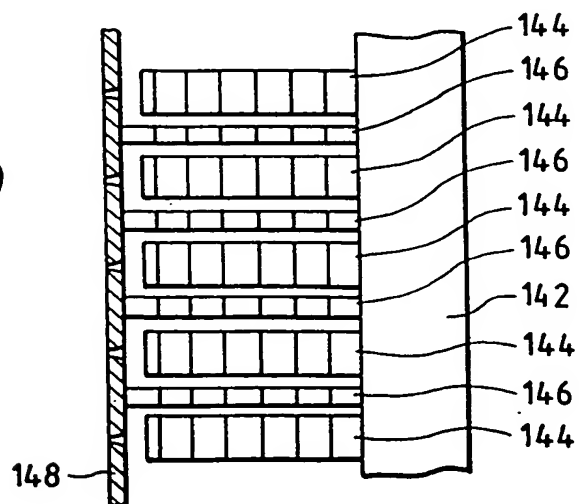






FIG. 22a

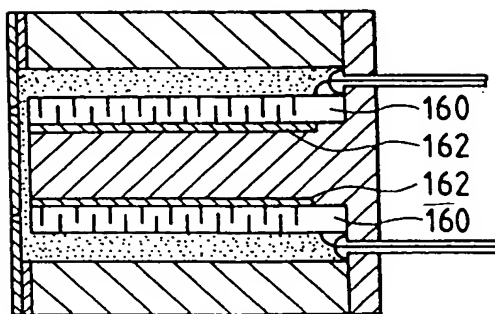


FIG. 22b

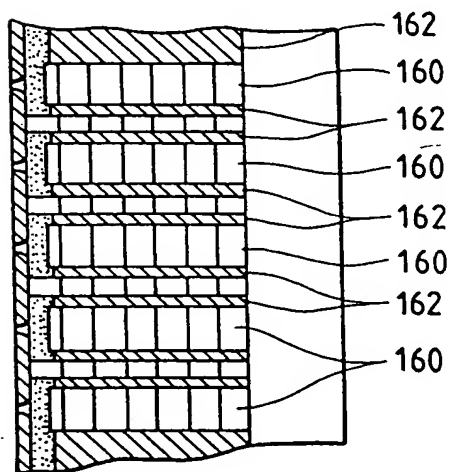
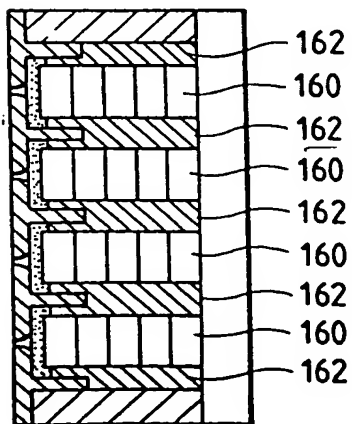


FIG. 22c



A schematic diagram of a multi-channel device, likely a microfluidic chip. It consists of a rectangular substrate with a central vertical channel (176) and two rows of horizontal channels (172 and 174) on either side. The channels are connected by a series of vertical lines. The entire device is enclosed in a frame (170). The label 168 points to the right side of the device.

FIG. 24a

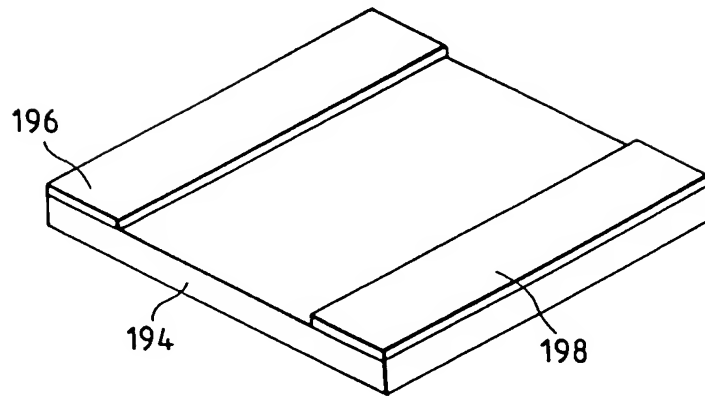


FIG. 24b

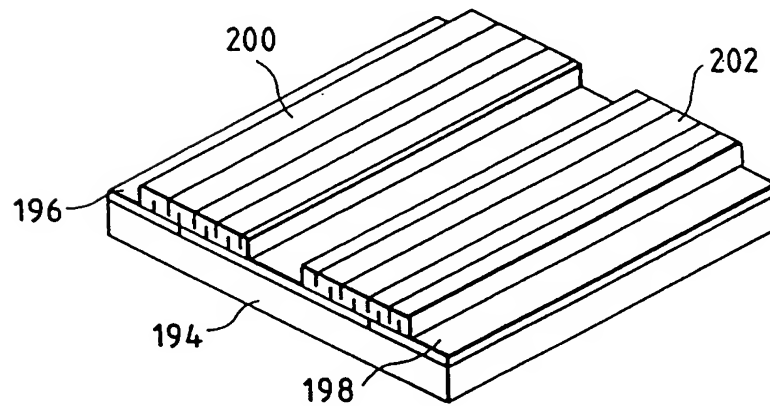


FIG. 24c

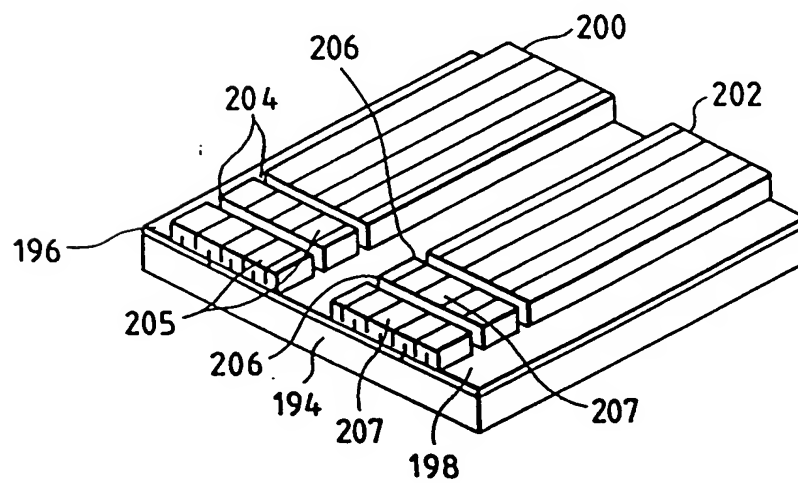


FIG. 25a

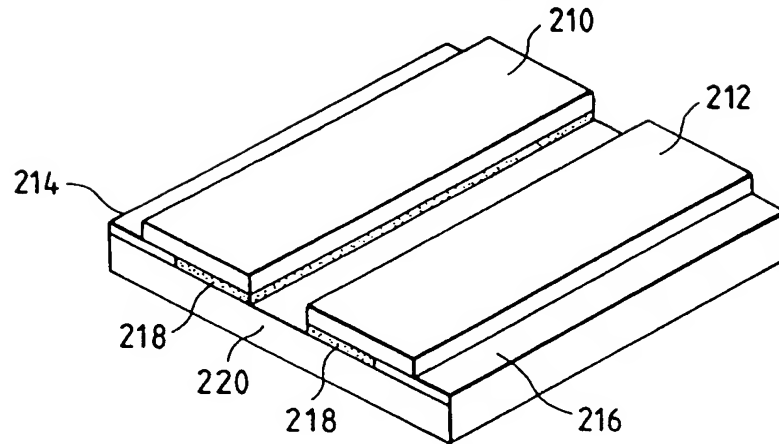


FIG. 25b

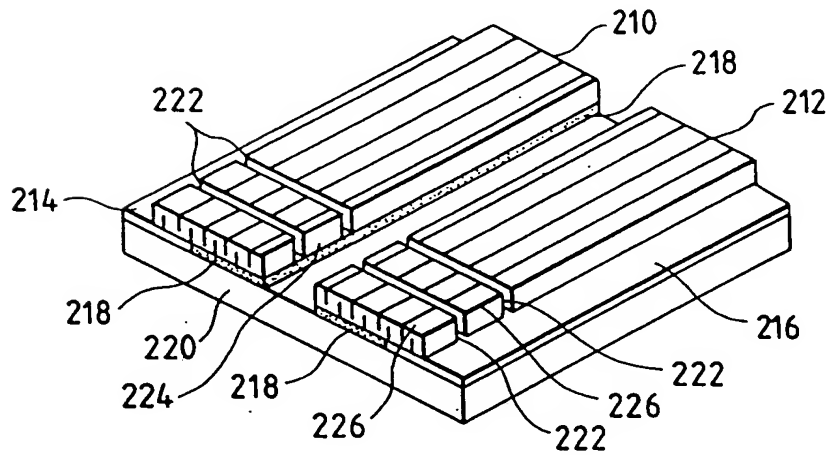


FIG. 26

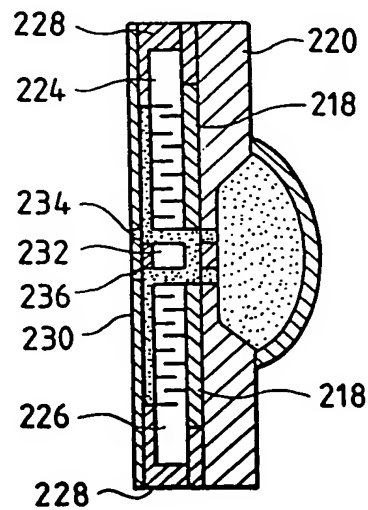


FIG. 27a

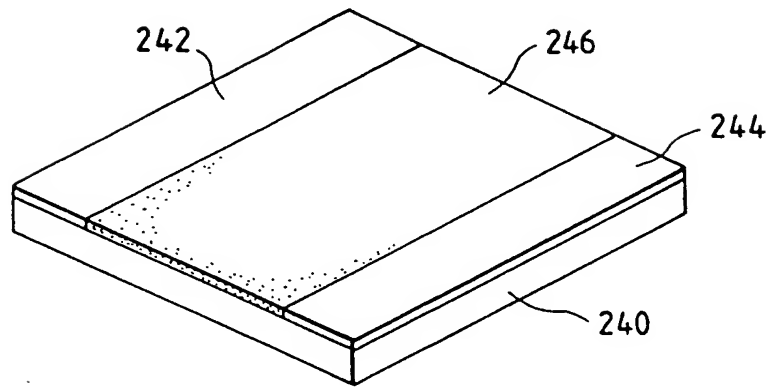


FIG. 27b

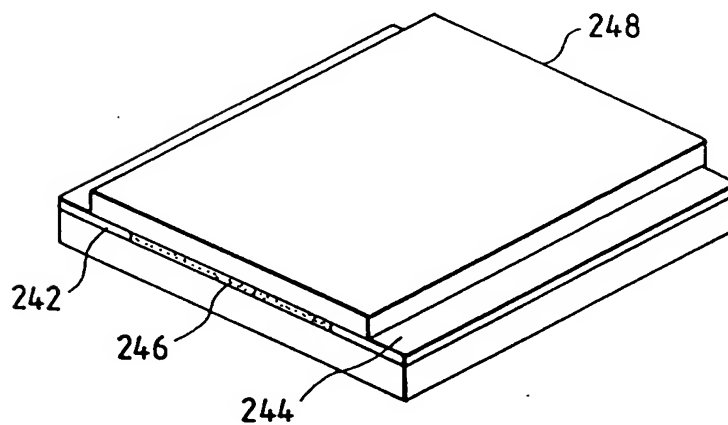


FIG. 27c

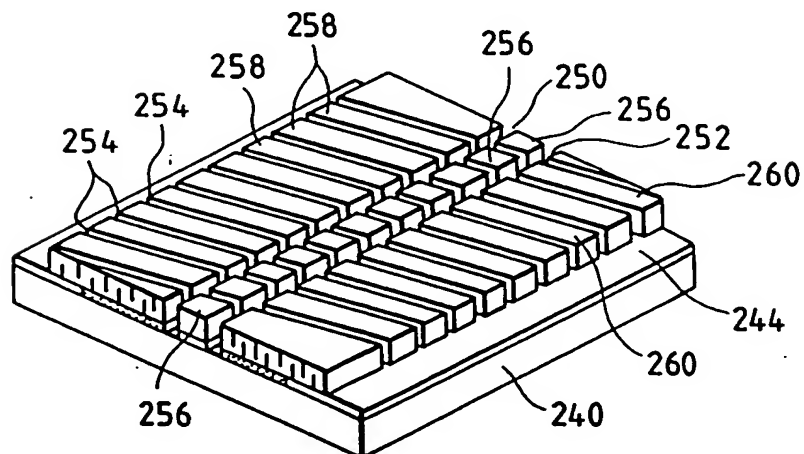


FIG. 28

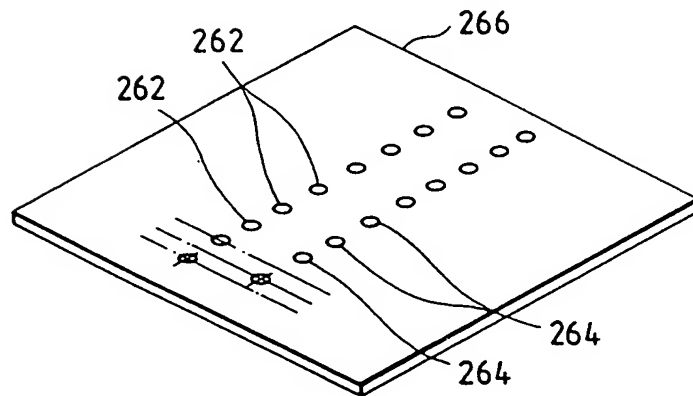


FIG. 29

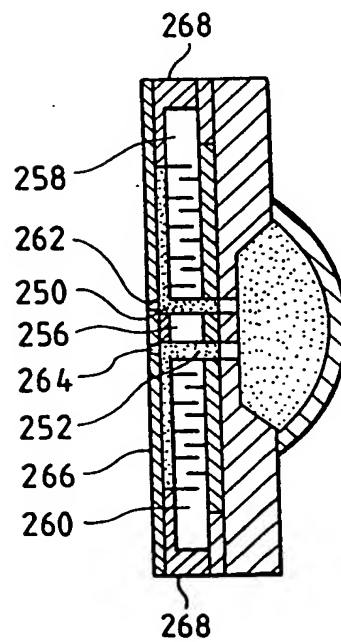


FIG. 30a

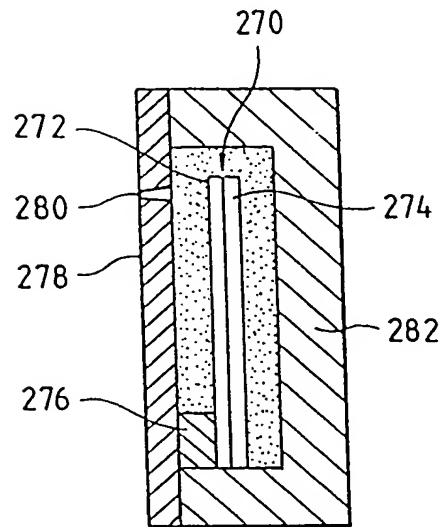
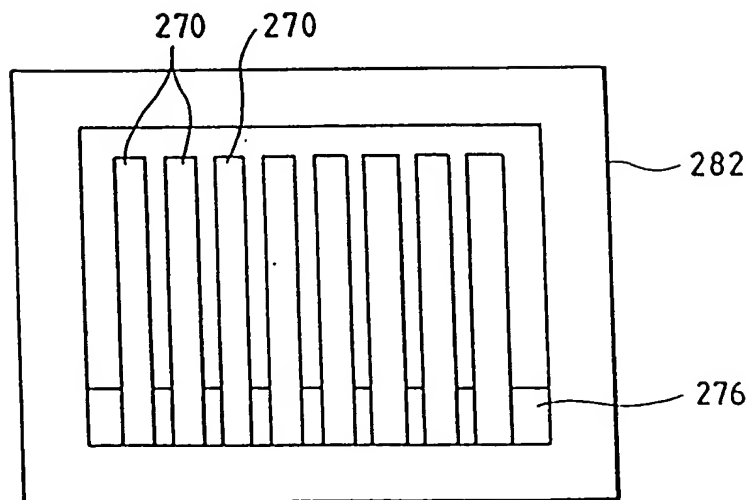
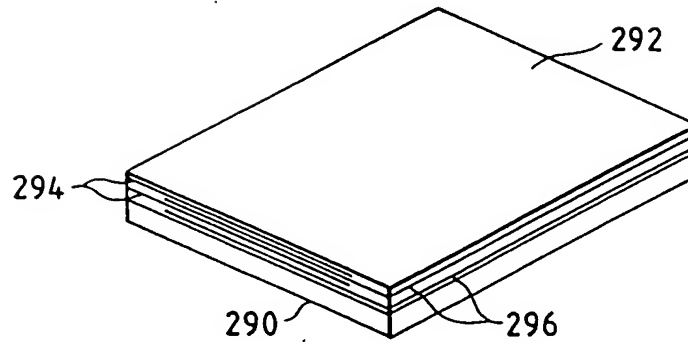


FIG. 30b

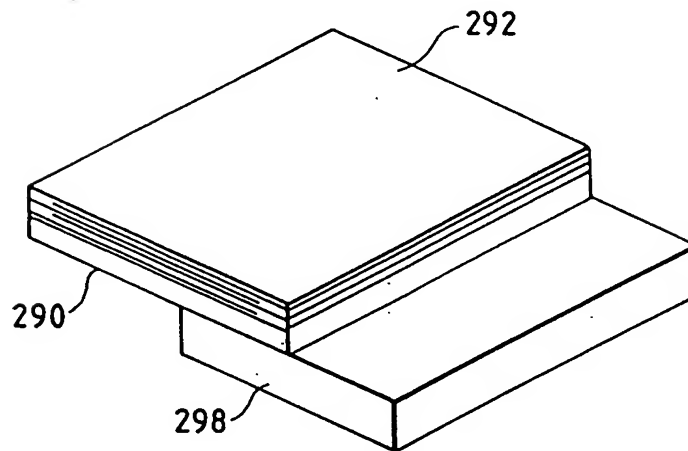




*FIG. 31a*



*FIG. 31b*



*FIG. 31c*

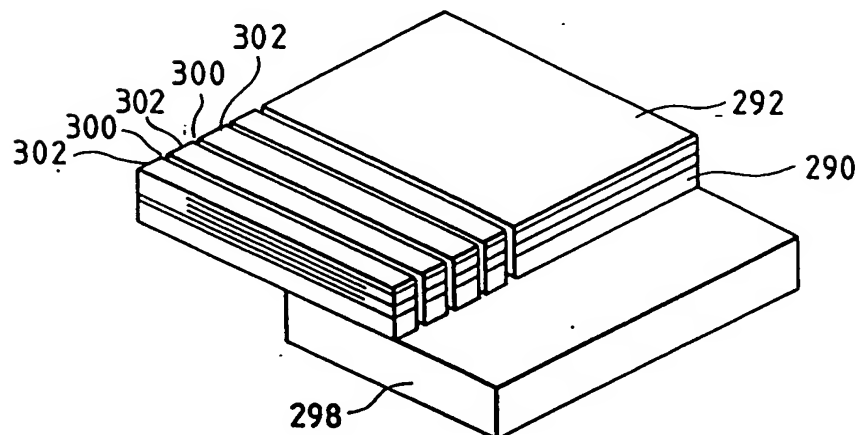


FIG. 32a

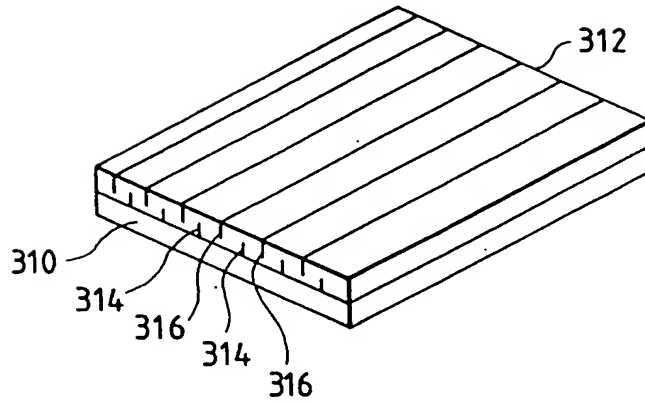


FIG. 32b

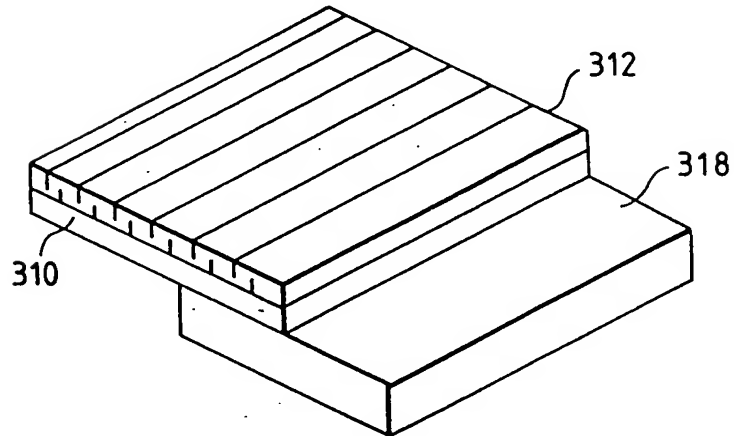


FIG. 32c

